

06/26/01

ISSUE CLASSIFICATION	
Class	Subclass

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D.I.P.E. <i>AK</i> <i>lup</i> <i>Q</i> SCANNED <i>Q</i> G.A. <i>Am</i>	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/891157	D ✓	<del>365</del> 257	200 <sup>+</sup>	2818 <del>2814</del>	11. 11. 11

Tyler Lowrey  
Patrick Kiersy  
Stephen Hudgens  
Jon Maimon

Method for making programmable resistance memory element

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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